

Bhoj Reddy Engineering College for Women
Department of Electronics & Communication Engineering
Time Table for the Academic Year 2015-16
III B.Tech II Semester ECE Section-B

Room No : 305 North Block (NB)

w.e.f : 21-12-2015

Day	930-1030	1030-1130	1130 - 1230	1230 - 1330	1330 - 1430	1430-1530	1530-1630
Monday	←-----MPMC/DSP LAB-----→			LUNCH	DC	VLSI(G1-305) DC(G2-309) DSP (G3-311)	MPMC
Tuesday	DSP	MEFA	MEFA(G1-305) IPR(G2-309) MPMC (G3-311)		IPR	VLSI	DC
Wednesday	MEFA	IPR(G1-305) MPMC(G2-309) MEFA (G3-311)	IPR		←-----DSP/MPMC LAB-----→		
Thursday	IPR	MPMC	DC		DSP	DC(G1-305) DSP(G2-309) VLSI (G3-311)	VLSI
Friday	MPMC	VLSI	DSP		MEFA	MPMC(G1-305) MEFA(G2-309) IPR (G3-311)	IPR
Saturday	DC	VLSI	DSP(G1-305) VLSI(G2-309) DC (G3-311)		MEFA	MPMC	DSP

S.No	Subject		Faculty	Dept
1	MEFA	Managerial Economics & Financial Analysis	T.Sandhya Reddy	MBA
2	IPR	Intellectual Property Rights	Saba Sulthana	ECE
3	DC	Digital Communications	B.Jyothsna	ECE
4	VLSI	VLSI Design	S.Prashanthi	ECE
5	MPMC	Microprocessors & Microcontrollers	M.Krishna Chaitanya	ECE
6	DSP	Digital Signal Processing	Nasreen Sultana	ECE
7	MPMC LAB	Microprocessors & Microcontrollers Lab	M.Krishna Chaitanya, RVNR. Suneel Krishna	ECE
8	DSP LAB	Digital Signal Processing Lab	Nasreen Sultana, M.Swapna/K. Madhu Krishna	ECE

Tutorial Groups :

G1:461-84

G2: 485-4A6

G3: 4A7-L424